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January 16.

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01 FC:1501 02 FC:1504

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02/13/2004

PIRST NAMED INVENTOR Till Schloesser

ATTORNEY DOCKET NO. INF-135

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TITLE OF INVENTION: METHOD OF FABRICATING AND ARCHITECTURE FOR VERTICAL TRANSISTOR CELLS AND TRANSISTOR-CONTROLLED MEMORY CELLS

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HUYNH, ANDY		2818	***	257-302000	-		
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Facsimile Number: (571) 273-2885

Transmission Date:

January 16, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Schloesser, et al.

Docket No.:

INF-135

Serial No:

10/777,128

Art Unit:

2818

Date Filed:

February 13, 2004

Title:

Architecture for Vertical Transistor Cells and Transistor-Controlled Memory

Cells

CERTIFICATION OF FACSIMILE TRANSMISSION

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